

ABSTRACT OF THE DISCLOSURE

In a system, device, and method for controlling access to a memory, a
5 memory interface device is used to coordinate access to a memory device by a
number of host applications. The memory interface device is situated
between the number of host applications and the memory device. The
memory interface device received memory access requests from the number
of host applications, interacts with the memory device for servicing the
10 memory access requests, and provides result/status information to the
number of host applications. The memory interface device maintains a
separate context for each memory access request in order to correlate each
memory access request with the host application that issued the memory
access request and the result/status information generated for the memory
15 access request.

1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99 100